

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	276	(LDD with graded)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:09
L6	163	I5 and (n adj channel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:31
L7	0	I6 and (decreasing with toward with channel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:10
L8	8	I6 and (decreasing with channel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:10
L9	821	257/303.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:40
L10	23	(thin adj film) and CMOS and (graded with LDD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:43
L11	2	I10 and driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:42
L12	1127	(thin adj film) and CMOS and (LDD) and (driver adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:44
S1	50	(((((LDD with gate with (overlying or overlapping))) and pixel) and TFT) and (driver adj circuit)) and (second adj gate adj electrode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:20
S2	39	S1 and (gate with second with layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:39

S3	2	S2 and (column with spacer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:36
S4	2	S1 and (column with spacer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:20
S5	66	column with spacer with pixel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:38
S6	22	S5 and (n-channel or p-channel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:39
S7	16	S6 and (gate with second with layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:42
S8	27	S5 and (gate with second with layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:42
S9	5	"6639265"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:12
S10	63	(((((LDD with gate with (overlying or overlapping)) and pixel) and TFT) and (driver adj circuit)) and (second adj gate adj electrode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:20
S11	2	S10 and (column with spacer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:22
S12	25	(column with shape with spacer with pixel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:32

S13	22	S12 and capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:33
S14	20	S13 and (n adj channel) and (p adj channel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:34
S15	5	"6639265"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/06 12:00
S16	1222	(n adj channel) and (p adj channel) and (driver adj circuit)and LDD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/06 12:03
S17	1	S16 and (LDD with graded)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:08
S18	357	(pixel adj (TFT or (thin adj film adj transistor))) with LDD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/06 13:07
S19	231	S18 and (storage adj capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/06 13:07